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PRINTER RUSH
(PTO ASSISTANCE)

Application : <u>10/824,072</u>	Examiner : <u>RAY</u>	GAU : <u>2111</u>
From: <u>UR</u>	Location: <u>(IDC) FMF FDC</u>	Date: <u>04-21-05</u>
Tracking #: <u>06086063</u>		Week Date: <u>03-14-05</u>

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449	_____	<input checked="" type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS	_____	<input type="checkbox"/> Foreign Priority
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<input type="checkbox"/> IIFW	_____	<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW	_____	<input type="checkbox"/> Other
<input type="checkbox"/> DRW	_____	
<input type="checkbox"/> OATH	_____	
<input type="checkbox"/> 312	_____	
<input type="checkbox"/> SPEC	_____	

[RUSH] MESSAGE: Application no. PCT/US90/01468 is listed
as which is a 371 in palm sheet, but missing
in specification.

Please correct specification.

Thank you
UR

[XRUSH] RESPONSE: _____

OK

INITIALS: MA

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.
REV 10/04

Amendments to the Specification

Please insert the following paragraph on page 1 beginning at line 4.

This is a continuation of United States patent application Serial No. 10/243,537 filed September 14, 2002; which is a continuation of application Serial No. 09/677,853 filed October 3, 2000; which is a continuation of application Serial No. 09/084,163 filed May 25, 1998, which issued November 28, 2000, as United States Patent No. 6,154,799; which is a continuation of application Serial No. 07/923,996 filed August 16, 1993, which issued May 26, 1998, as United States Patent No. 5,758,109,

*See
original
spec*

Please substitute the following paragraph for the paragraph which begins on page 5 in line 32.

As illustrated between the dashed lines 20 and 24 in FIG. 1, the ARBITRATION phase of the SCSI protocol begins when one or more devices ~~arbitrates~~ arbitrate for the bus by simultaneously asserting BSY and its address on the SCSI bus. The address asserted by each device during the ARBITRATION phase indicates that device's priority to all other devices on the bus. If a device's address on the DATA BUS corresponds to data bit 7, then that device has

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which is a 371 of PCT/US90/01468 filed March 19,1990 ,



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BIBDATASHEET

Bib Data Sheet

CONFIRMATION NO. 7007

SERIAL NUMBER 10/824,072	FILING DATE 04/13/2004 RULE	CLASS 710	GROUP ART UNIT 2111	ATTORNEY DOCKET NO. 1807CON5
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APPLICANTS

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James A. Moorer, San Rafael, CA, Legal Representative;

** CONTINUING DATA **

yes GCR 2/7/05

This application is a CON of 10/243,537 09/14/2002 ABN
which is a CON of 09/677,853 10/03/2000 ABN
which is a CON of 09/084,163 05/25/1998 PAT 6,154,799
which is a CON of 07/923,996 08/16/1993 PAT 5,758,109
which is a 371 of PCT/US90/01468 03/19/1990

** FOREIGN APPLICATIONS **

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/23/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY CA	SHEETS DRAWING 8	TOTAL CLAIMS 11	INDEPENDENT CLAIMS 3
Verified and Acknowledged Examiner's Signature _____ Initials <i>GCR 2/7/05</i>					

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TITLE

Network of computing devices including a repeater for distributed arbitration digital data buses

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